

FIG. 1

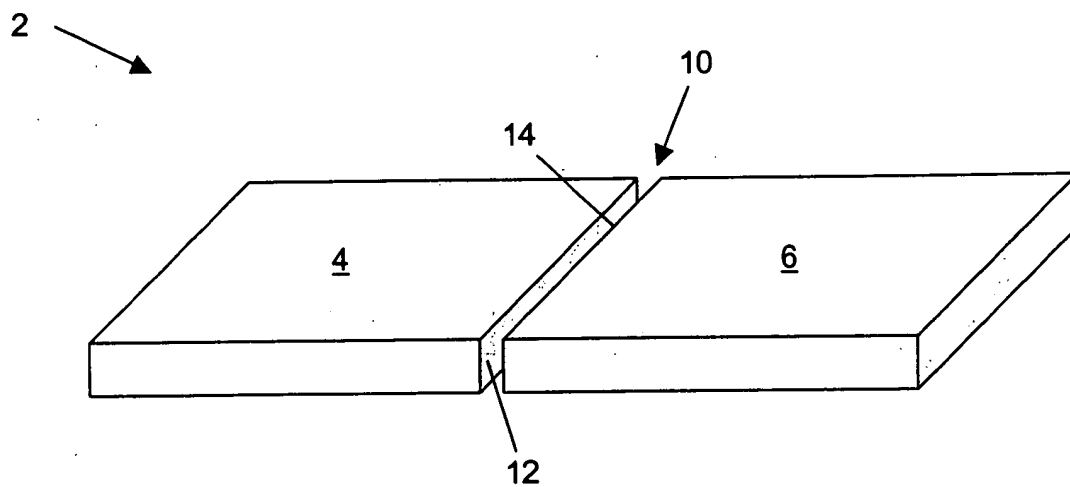
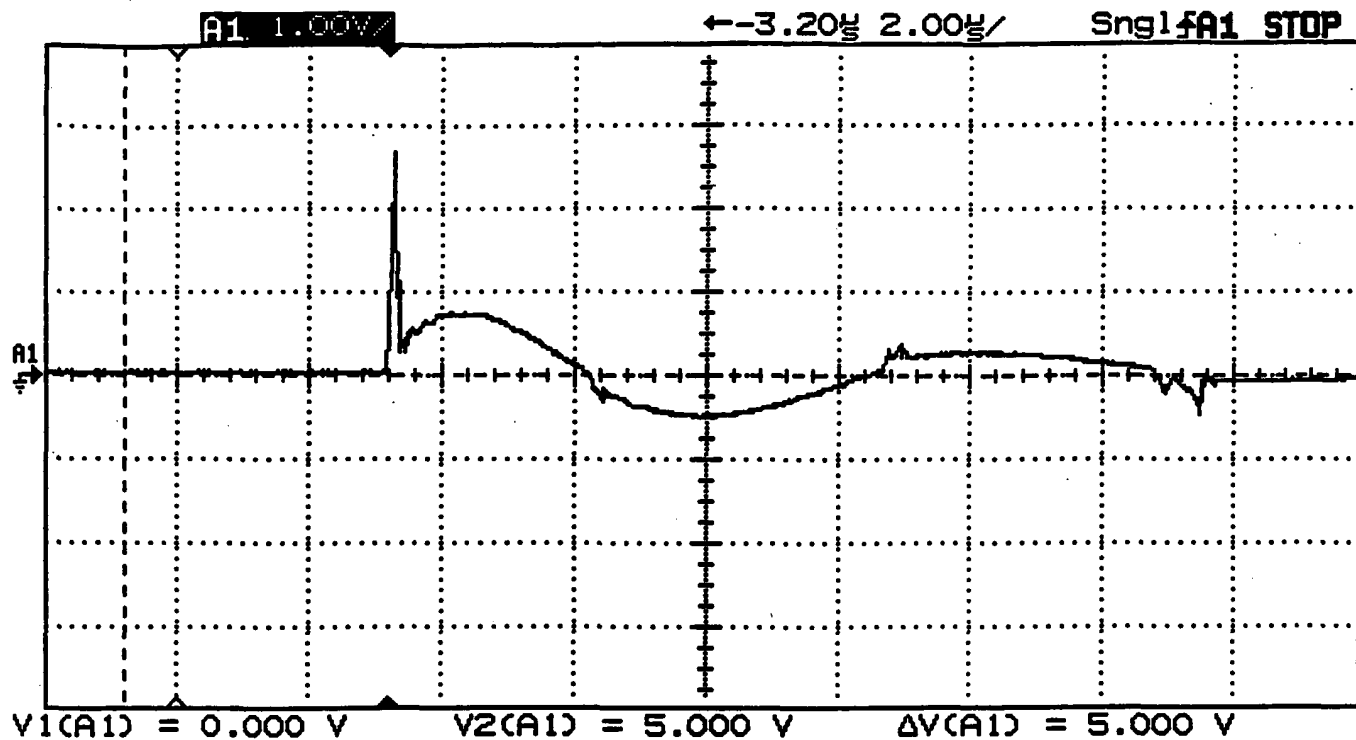


FIG. 2



Analog	State	Volts/div	Position	Coupling	BW Limit	Invert	Probe
A1	on	1.000 V	0.000 V	dc	off	off	1:1 A
A2	off	80.00 V	0.000 V	dc	off	off	10:1 A

Horizontal	Mode	Main s/div	Delay	Reference
	main	2.000us	-3.200us	left

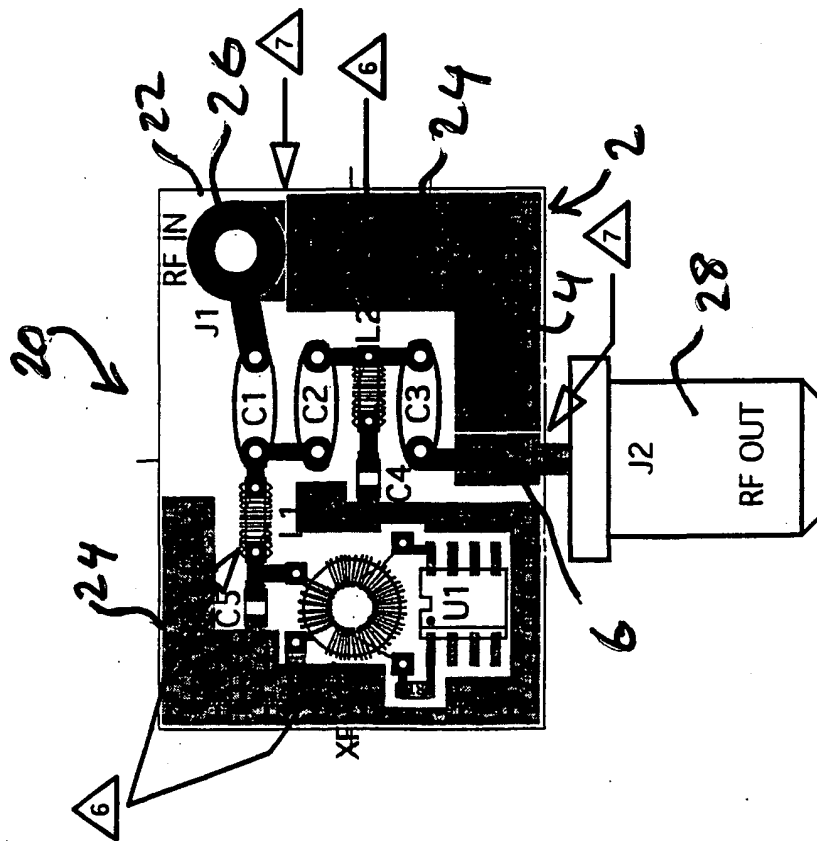
Trigger	Source	Level	Slope	Coupling	Rej	NoiseRej	Holdoff
edge	A1	0.000 V	+	dc	off	off	200.0ns

Display	Mode
	normal

Cursors	V1(A1)	V2(A1)	dV(A1)
	0.000 V	5.000 V	5.000 V
	t1 = -4.000us	t2 = 96.00us	dt = 100.0us
			1/dt = 10.00kHz

FIG.3

Plot 1



- △ 1 DOUBLE-SIDED
- △ 2 ALL HOLES PLATED THRU.
- △ 3 1oz. / 1oz. Cu.
- △ 4 SMOBC
- △ 5 OMIKRON/WHITE TIN PLATING
- △ 6 APPLY DOPING/POTTING COMPOUND TO THIS AREA.
- △ 7 CONTROL GAP TO 0.002" +/- 0.0005"

FIG. 4